

Fig.1 (a)

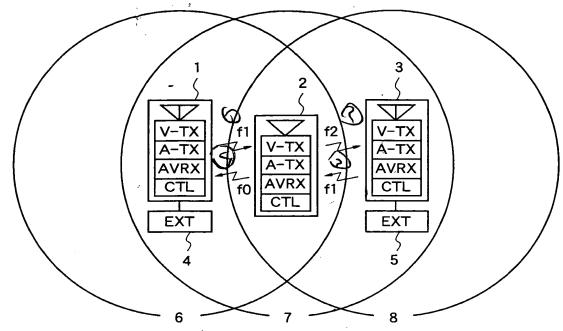
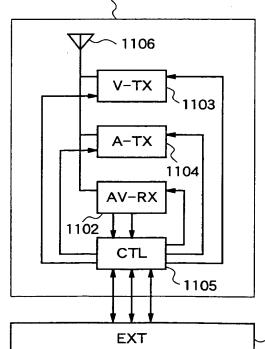


Fig.1 (b)

The second secon



1101

1: master station

2: relay station

3: slave station

4,5: terminals

1101: body of each station

1102 : station selection/video audio

demodulation circuit

1103 : high-frequency

video modulation circuit

1104: high-frequency

audio modulation circuit

1105 : control circuit

1106: transmission/reception

antenna

1107

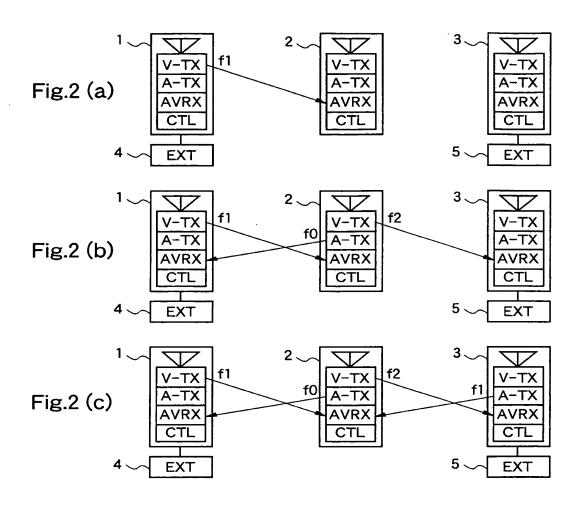
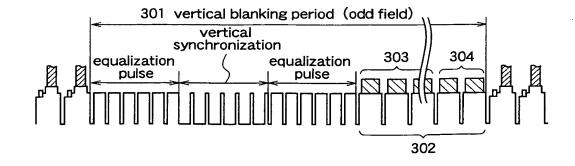


Fig.3



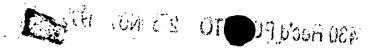
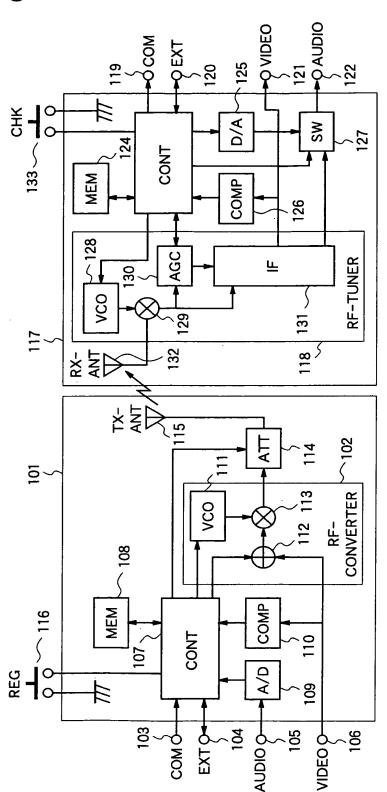


Fig. 4



506(105,122,109,125,107,123,110,126,112,127): audio signal superposition control means 505(104,120,107,123,110,126,112): control signal superposition means 500(118,123,124,126,133): available frequency detection means 504(106,110,126,107,123): frequency changing means

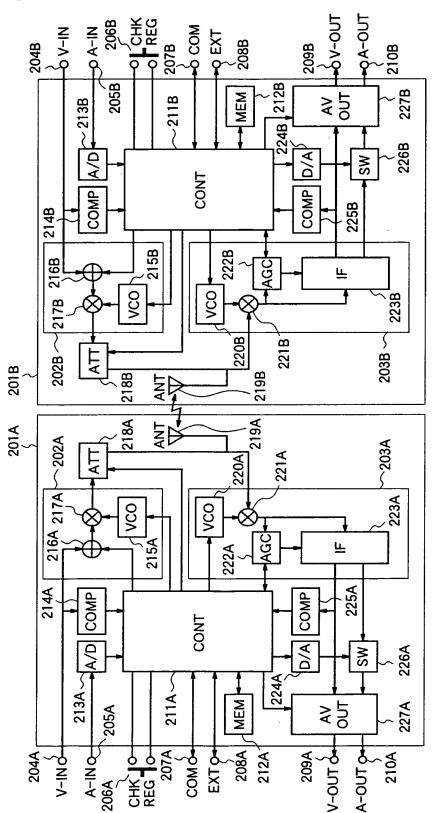
502(107,123,108,124,102,118) : spread spectrum communication means 503(107,108,114) : transmission power control means

501(103,119,107,123,108,124,116): frequency registration means

101 : transmission apparatus 117 : receiving apparatus



Fig. 5



523(219A,219B,203A,203B,225A,225B,211A,211B,212A,212B): retransmission means 524(211A,211B,225A,225B,212A,212B,227A,227B): output stop means

522(203A, 203B, 225A, 225B, 211A, 211B, 212A, 212B): frequency setting means 520(207A, 207B, 211A, 211B, 212A, 212B): ID storage means 520(207A, 207B, 207B, 211A, 211B, 212A, 212B): ID inquiry/registration means 521(206A, 206B, 207A, 207B, 211B, 212A, 212B): ID inquiry/registration means 510(211A, 212A): frequency changing order control means 511(211A, 212A): communication control means 512(211A, 212A, 214A, 225A, 216A): communication frequency list updation means

Fig. 6

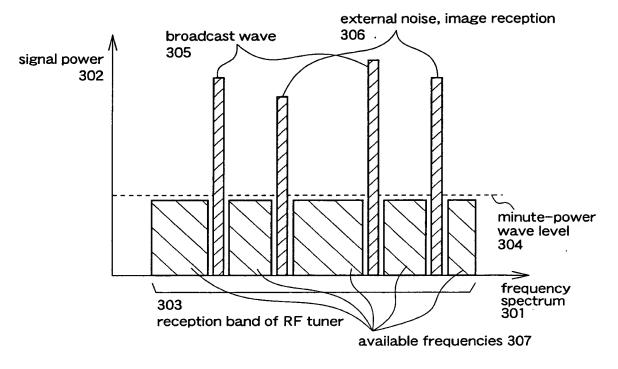
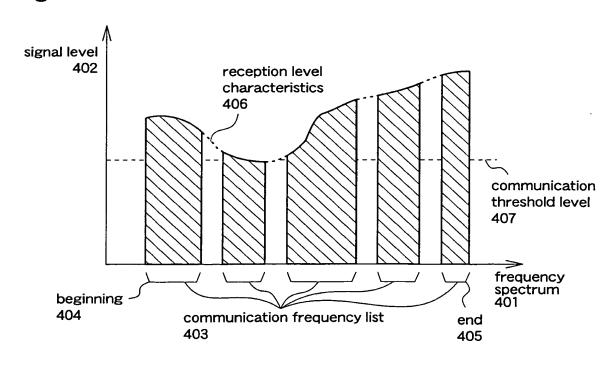


Fig. 7



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Fig.8 (a) received image according to conventional example

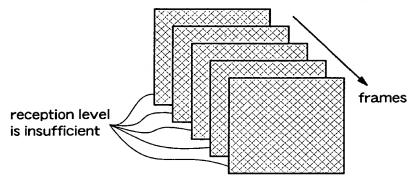


Fig.8 (b) received image according to embodiment 2 of invention

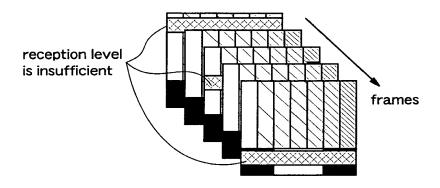


Fig.8 (c)
received image according to embodiment 3 of invention

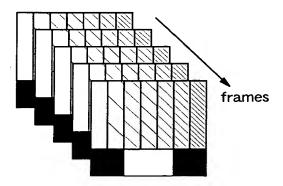
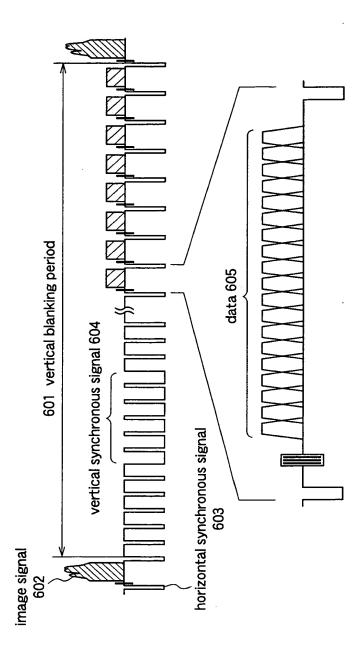


Fig. 9



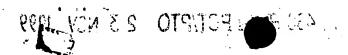


Fig.10

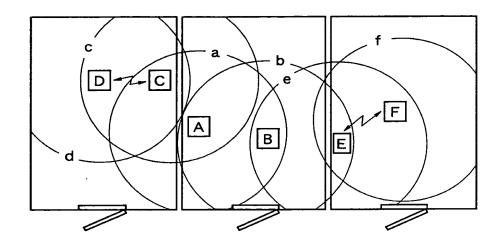


Fig.11

